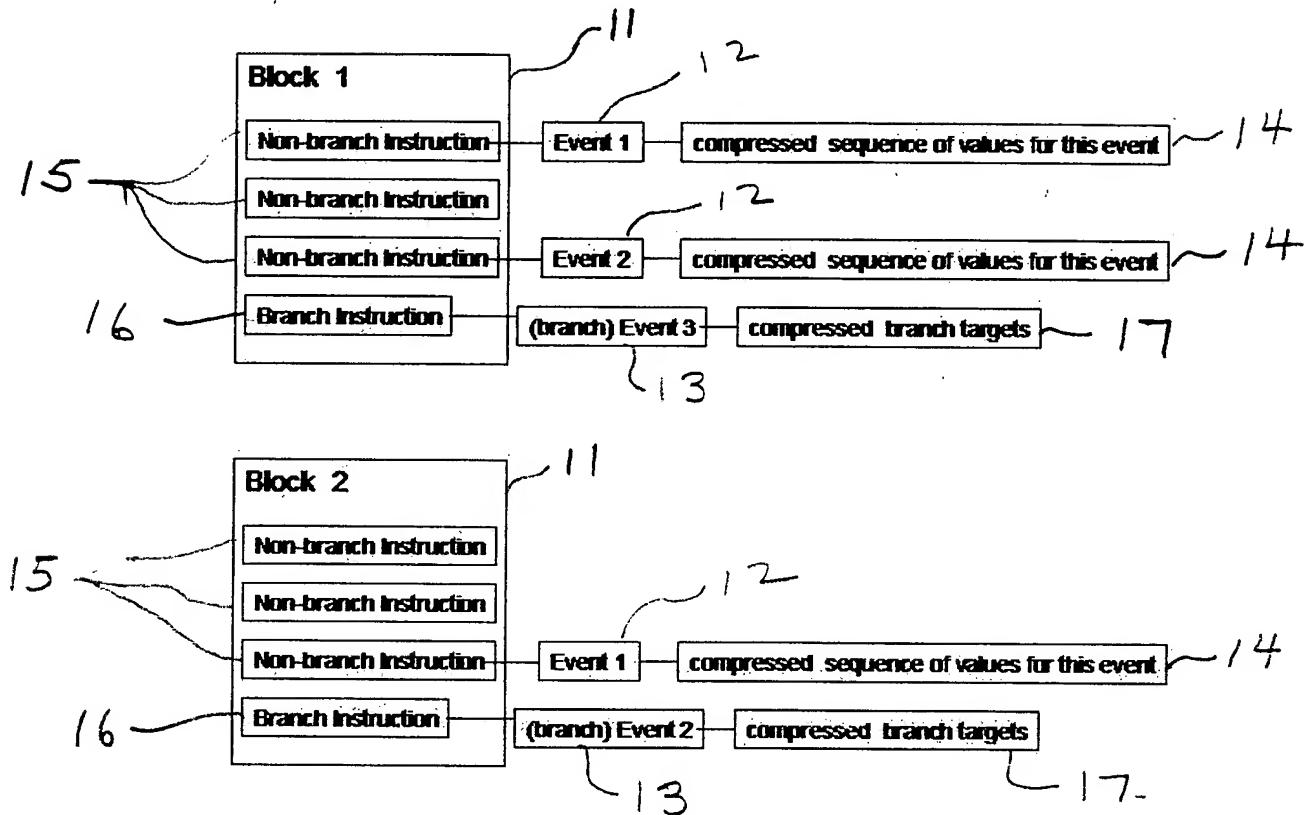
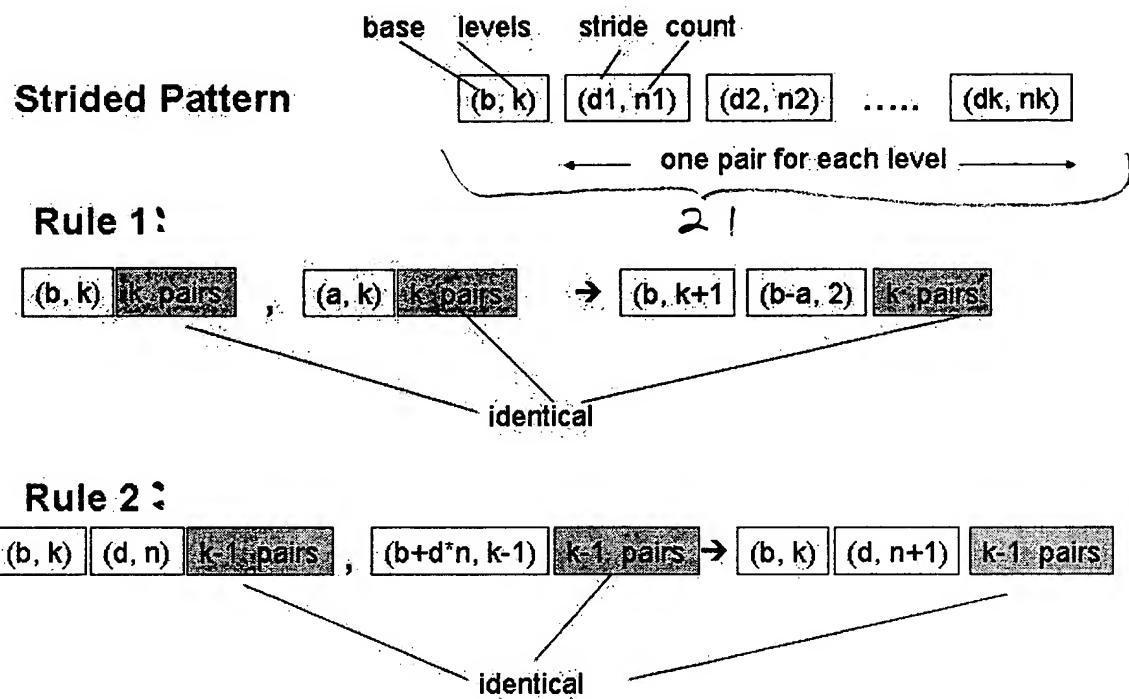


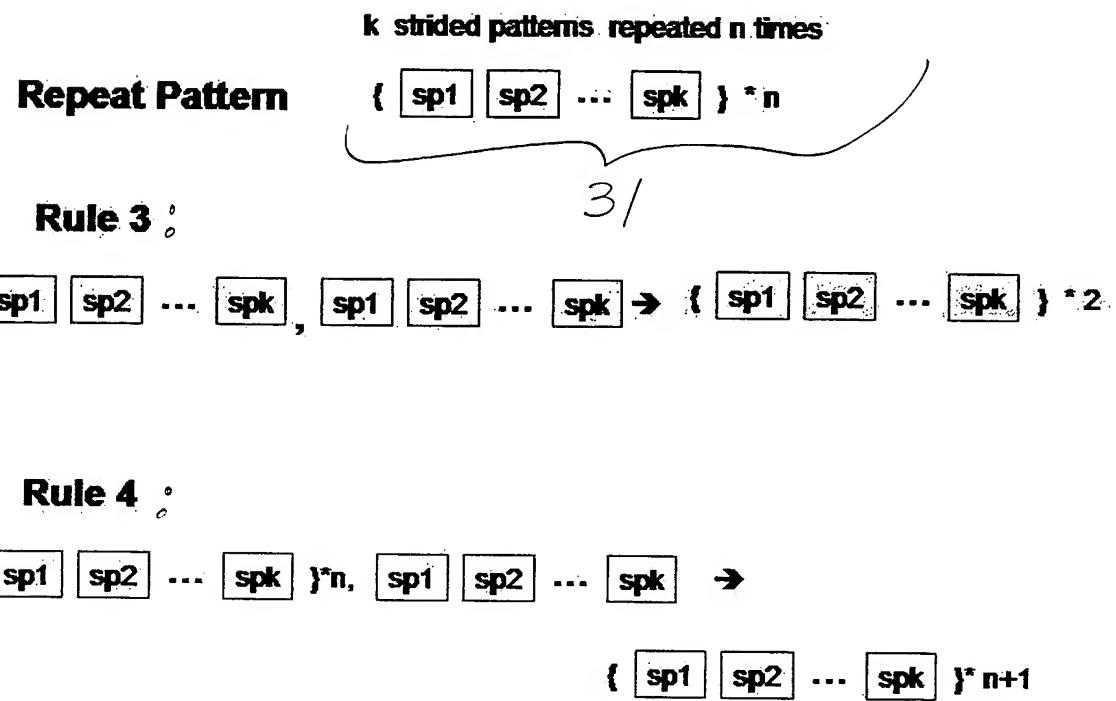
**Figure 1 Blocks, Instructions, Events and Compressed Value-Sequences**



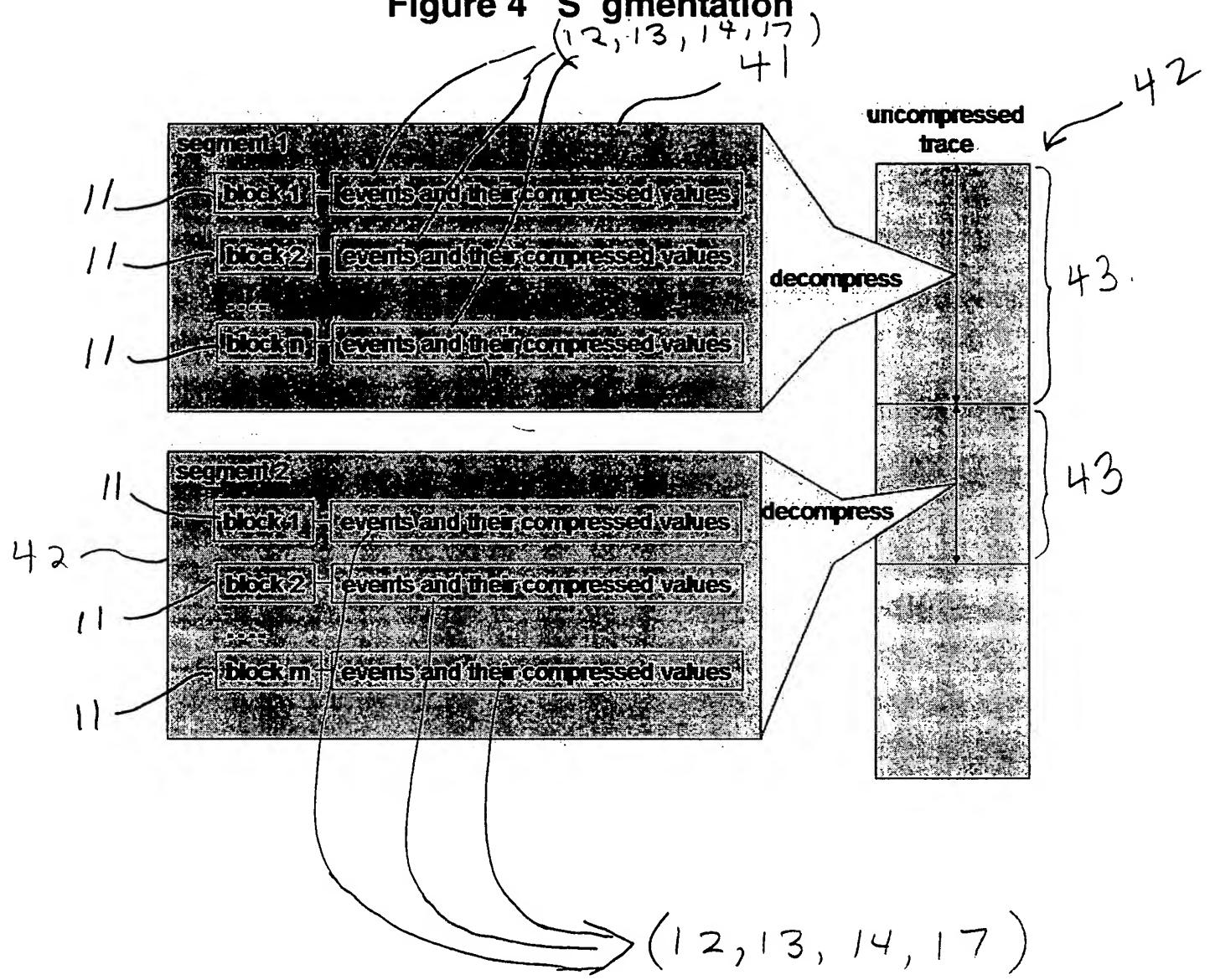
**Figure 2 Stride Pattern Reduction Rules**



**Figure 3 Repeat Pattern Reduction Rules**



**Figure 4 Segmentation**



**Figure 5 Address Pre-fetching**

